

IN THE U.S. PATENT AND TRADEMARK OFFICE

I N F O R M A T I O N S H E E T

Applicant: KISHIMOTO, Kiyoharu
FUKAO, Ryuzo
YAMAGUCHI, Koji
TSUKAMOTO, Hiroyuki
YAMASHITA, Yuji
KIKUCHI, Yuji
KANAI, Tomonori

Application No.:

Filed: July 12, 2001

For: SEMICONDUCTOR MODULE AND PRODUCING METHOD THEREFOR

Priority Claimed Under 35 U.S.C. 119 and/or 120:

| COUNTRY | DATE | NUMBER |
|---------|----------|-------------|
| JAPAN | 07/12/00 | 2000-212068 |
| JAPAN | 11/10/00 | 2000-343688 |
| JAPAN | 03/15/01 | 2001-074590 |

Send Correspondence to: BIRCH, STEWART, KOLASCH & BIRCH, LLP
P. O. Box 747
Falls Church, Virginia 22040-0747
(703) 205-8000

The above information is submitted to advise the USPTO of all relevant facts in connection with the present application. A timely executed Declaration in accordance with 37 CFR 1.64 will follow.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By


JOHN W. BAILEY

Reg. No. 32,881

P. O. Box 747

Falls Church, VA 22040-0747

/kw

(703) 205-8000